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Dated: Sept. 20, 2005

Signature

(Lawrence E. Russ)

Docket No.: VELOX 3.0-001
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Pophrastic et al.

Application No.: 10/780,526

Group Art Unit: 2811

Filed: February 17, 2004

Examiner: S.A. Gebremariam

For: LOW DOPED LAYER FOR NITRIDE-BASED
SEMICONDUCTOR DEVICE

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This communication is in response to the Office Action mailed August 1, 2005, setting forth a Restriction Requirement in the above-identified application. In the Office Action, the Examiner required restriction to one of the following inventions under 35 U.S.C. §121:

I. Claims 1-36 and 71-73, drawn to a method of forming an integrated circuit device.

II. Claims 37-70 and 74-76, drawn to a semiconductor integrated circuit device.

In response, Applicants hereby elect the invention of Group I, corresponding to claims 1-36 and 71-73. Applicants reserve the right to file a divisional application corresponding to the non-elected claims.

Application No.: 10/780,526

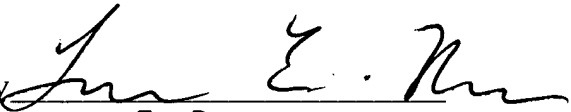
Docket No.: VELOX 3.0-001

In the event any fee is due in connection with the present response, the Examiner is authorized to charge Applicant's Deposit Account No. 12-1095 therefor.

Dated: September 20, 2005

Respectfully submitted,

By



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